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APPLICATION NO.	F	LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/714,497 11/14/2003		11/14/2003	Motoaki Wakui	81784.0293	1021
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HOGAN & HARTSON L.L.P. 500 S. GRAND AVENUE				TRINH, MICHAEL MANH	
SUITE 1900		10L		ART UNIT	PAPER NUMBER
LOS ANGELES, CA 90071-2611			***	2822	

DATE MAILED: 04/20/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)					
	Office Astion Communication	10/714,497	WAKUI ET AL.	m				
	Office Action Summary	Examiner	Art Unit					
		Michael Trinh	2822					
Period fo	The MAILING DATE of this communication app or Reply	pears on the cover sheet with the c	orrespondence addr	ess				
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DATE of the may be available under the provisions of 37 CFR 1.1: SIX (6) MONTHS from the mailing date of this communication. O period for reply is specified above, the maximum statutory period vere to reply within the set or extended period for reply will, by statute, reply received by the Office later than three months after the mailing ed patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this comm 0 (35 U.S.C. § 133).					
Status								
1)	Responsive to communication(s) filed on 09 Fe	ebruary 2006.						
2a)□		action is non-final.						
3)□	· · · · · · · · · · · · · · · · · · ·							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Dispositi	on of Claims							
4)⊠	Claim(s) 1 and 3-5 is/are pending in the application	ation.						
	4a) Of the above claim(s) is/are withdrawn from consideration.							
	Claim(s) is/are allowed.							
6)⊠	☑ Claim(s) <u>1 and 3-5</u> is/are rejected.							
	Claim(s) is/are objected to.							
8)□	Claim(s) are subject to restriction and/or	r election requirement.						
Applicati	on Papers							
9)□	The specification is objected to by the Examine	r						
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,	10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
	Replacement drawing sheet(s) including the correcti		· ·	1 121(d)				
11)	The oath or declaration is objected to by the Ex							
	ınder 35 U.S.C. § 119							
12) 🗌	Acknowledgment is made of a claim for foreign ☐ All b)☐ Some * c)☐ None of:	priority under 35 U.S.C. § 119(a)	-(d) or (f).					
aرد	1. Certified copies of the priority documents	s have been received						
	2. Certified copies of the priority documents		on No					
	3. Copies of the certified copies of the prior							
	application from the International Bureau		d in this National St	age				
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	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)	4) Interview Summary (Paper No(s)/Mail Da						
	nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	5) Notice of Informal Pa		52)				
	No(s)/Mail Date	6) Other:						

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DETAILED ACTION

*** This office action is in response to Applicant's Amendment and RCE filed on February 06 and 09, 2006. Claims 1,3-5 are pending.

*** The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

Claim Rejections - 35 USC § 103

1. Claim 1 is rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted prior art in view of Boucher et al (6,354,909) and Kaburagi et al (6,221,814).

Applicant's admitted prior art teaches (at Figures 4, 5 S10-S22) a semiconductor device manufacturing method, comprising: a first step of forming a laminated structure by adhering, on a semiconductor substrate 10 including a plurality of integrated circuits, a carrier member 2,3 covering a region in which the plurality of integrated circuits are formed, with an insulating resin 5 interposed between the semiconductor substrate 10 and the carrier member 2,3 (Figure 5 S12); a second step of cutting on the laminated structure so as to cut the semiconductor substrate together with the insulating resin 5 while allowing at least a portion of the carrier member 2 to remain uncut (Fig 5 S14); a third step of forming metal wiring 28 on a machined surface of the laminated structure (Fig 5 S16); a fourth step of dividing the laminated structure by cutting the carrier member (Fig 5 S22); wherein the second step is performed with a dicing saw used to cut into the laminated structure including the semiconductor substrate (Figs 5 S22 and 6).

Re claim 1, Applicant's admitted prior art lacks cooling the dicing saw and a cutting portion at a temperature lower than the softening temperature of the resin, by spraying a coolant liquid on the dicing saw, which coolant liquid has a pH from 6 to 8.

However, Boucher teaches (at Figures 1-2; col 14, lines 21-64) cooling the dicing saw and the cutting portion while cutting the semiconductor substrate by spraying a coolant liquid from the nozzle 44 on the dicing saw 18, wherein the coolant temperature is less than about 50 degrees Fahrenheit (col 14, lines 53-64; 21-64) in order to improve cutting performance. Kaburagi teaches (at col 8, lines 28-31; col 1 and col 7, lines 9-67) saw dicing a semiconductor substrate with the use of cutting liquid for cleaning and cooling, wherein the cutting coolant

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liquid has a pH of about 6.5 (col 8, lines 28-31).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified the dicing method of Applicant's admitted prior art by cooling the dicing saw used to cut into the laminated structure including the semiconductor substrate, as taught by Boucher, at the coolant temperature of about 50 degrees Fahrenheit or less (less than 10 degrees Celsius), which temperature is lower than the softening temperature of the insulating resin as taught by Applicant's admitted prior art. This is because of the desirability to reduce temperature of the semiconductor substrate at the cutting portion and the dicing saw, and because of the desirability to remove particles generated during cutting, and improve cutting performance, wherein the coolant temperature of about 50 degrees Fahrenheit or less (less than 10 degrees Celsius), as taught by Boucher, is very low, and lower than the softening temperature of the insulating resin as taught by Applicant's admitted prior art,

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified the dicing method of the references including Applicant's admitted prior art and Boucher by spraying coolant with a spraying width larger than the width of the dicing saw with the coolant having a pH value of about 6.5, as taught by Kaburagi. This is because of the desirability to flood the coolant liquid to the entire width of the dicing saw so that to cool the dicing saw in an effective manner. This is also because of the desirability to reduce impact on the working environment and to prevent precipitates and residue particles from becoming a hard cake. Selecting the portion of the prior art's range of pH of 6.5 for coolant liquid, as shown by Kaburagi, which is within the range of applicant's claims, because it has been held to be obvious to select a value in a known range by optimization for the best results, and would be an unpatentable modification, where the general conditions of a claim are disclosed in the prior art, it is not inventive to discover the optimum or workable ranges by routine experimentation". In Re Aller 104 USPQ 233,255 (CCPA 1955); In re Waite 77 USPQ 586 (CCPA 1948); In Re Swanson 56 USPQ 372 (CCPA 1942); In Re Sola 25 USPQ 433 (CCPA 1935); and In Re Dreyfus 24 USPQ 52 (CCPA 1934).

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2. Claims 3,4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted prior art in view of Boucher et al (6,354,909) and Kaburagi et al (6,221,814), as applied to claim 1 above, and further of Sutherland et al (5,461,008).

The references including Applicant's admitted prior art and Boucher teach a semiconductor device manufacturing method as applied to claim 1 above.

Re claim 4, the references lack spraying the coolant with a spraying width larger than the width of the dicing saw; and Re claim 3, spraying the coolant at an angle elevation of from 5 and 45 degrees.

However, re claim 4, Sutherland teaches (at Fig 2, col 2, lines 30-40; col 3, lines 1-29) spraying the coolant to the dicing saw while cutting the semiconductor substrate, wherein the coolant is sprayed with a spraying width larger than the width of the dicing saw (Figs 1-2). Re claim 3, Boucher teaches (at Figures 1 and 2; col 14, lines 21-45) to provide coolant streams to the cutting edges of the dicing saw, wherein the nozzles 44 for spraying the coolant, as shown in Figures 1-2, are elevated at an angle elevation of about 45 degrees. Sutherland also teaches to spray coolant to the dicing saw, wherein the coolant is spraying at an angle elevation of less than 45 degrees from the nozzle 46a (Figs 1-2).

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to have modified the dicing method of the references including Applicant's admitted prior art and Boucher by spraying coolant with a spraying width larger than the width of the dicing saw with the coolant. This is because of the desirability to flood the coolant to the entire width of the dicing saw so that to cool the dicing saw in an effective manner.

Furthermore, the subject matter as a whole would have been obvious to one or ordinary skill in the art at the time the invention was made to select the portion of the prior art's range of angle for spraying the coolant from the nozzles to the dicing saw at an angle less than about 45 degrees, as shown by Boucher and Sutherland, which is within the range of applicant's claims, because it has been held to be obvious to select a value in a known range by optimization for the best results, and would be an unpatentable modification, where the general conditions of a claim are disclosed in the prior art, it is not inventive to discover the optimum or workable ranges by routine experimentation". *In Re Aller* 104 USPQ 233,255 (CCPA 1955); *In re Waite* 77 USPQ 586 (CCPA 1948); *In Re Swanson* 56 USPQ 372 (CCPA 1942); *In Re Sola* 25 USPQ 433

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(CCPA 1935); and In Re Dreyfus 24 USPQ 52 (CCPA 1934).

5. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Applicant's Admitted prior art in view of Boucher (6,354,909), and Kaburagi (6,221,814), and Sutherland (5,461,008), as applied to claims 1,3-4 above, and further of Cook (6,454,190).

The references including Applicant's admitted prior art, Boucher and Sutherland teach a semiconductor device manufacturing method as applied to claims 1,3,4 above, wherein the coolant includes water (Sutherland, Abstract, col 6, lines 27-45; col 1, lines 28-36).

Re claim 5, the references already teaches using water as a coolant, but lack using RO water by passing tap water through an RO (reverse osmosis) film.

However, Cook teaches (at Abstract; col 2, lines 45-65) providing pure RO water by passing tap water through an reverse osmosis (RO) membrane film 38.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to spray the water coolant to the dicing saw of the combined references by passing using passing the tap water through an reverse osmosis film as taught by Cook. This is because of the desirability to have high quality pure RO water as a coolant for cooling the dicing saw and the substrate and for removing residue particles, wherein clogging of the nozzle is also reduced as the pure RO water having very low mineral content.

Response to Amendment

** Applicant's arguments filed February 06 and 09, 2006 with respect to pending claims have been considered but are moot in view of the new ground(s) of rejection.

*** Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael M. Trinh whose telephone number is (571) 272- 1847. The examiner can normally be reached on M-F: 8:30 Am to 5:00 Pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zandra Smith can be reached on (571) 272-2429. The fax phone number is (571) 273-8300

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

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Michael Trinin Primary Examiner

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